

Module Handbook (<https://modhb.uni-kl.de/>)

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Module EIT-EIS-561-M-7

Embedded Systems Project (M, 15.0 LP)

Module Identification

Module Number	Module Name	CP (Effort)
EIT-EIS-561-M-7	<i>Embedded Systems Project</i>	15.0 CP (450 h)

Hint concerning Module EIT-EIS-561-M-7:
4 - 15 CPs depending on effort

Basedata

CP, Effort	15.0 CP = 450 h
Position of the semester	1 Sem. in WiSe/SuSe
Level	[7] Master (Advanced)
Language	[DE/EN] German or English as required
Module Manager	Kunz, Wolfgang, Prof. Dr.-Ing. (PROF DEPT: EIT) (/staff/344/)
Lecturers	Kunz, Wolfgang, Prof. Dr.-Ing. (PROF DEPT: EIT) (/staff/344/)
Area of study	[EIT-EIS] Electronic Design Automatization
Reference course of study	[EIT-88.781-SG#2010] M.Sc. Electrical and Computer Engineering [2010] (/mhb/FB-EIT/cos-556/)
Lifecycle-State	[NORM] Active

Courses

Type/SWS	Course Number	Choice in Module-Part	SL	PL	CP	Sem.
2S	EIT-EIS-561-K-7 (/mhb/courses/EIT-EIS-561-K-7/)	P	PROJ-Schein	PL1	15.0	WiSe/SuSe

- About [EIT-EIS-561-K-7]: Title: "Embedded Systems Project"; Presence-Time: 28 h; Self-Study: 422 h
- About [EIT-EIS-561-K-7]: The study achievement [PROJ-Schein] **proof of successful completion of the project(s)** must be obtained. It is a prerequisite for the examination for PL1.

Examination achievement PL1

- Form of examination: **combination of talk and written elaboration**
- Examination Frequency: each semester

Weighting between talk and written elaboration is set individually

Evaluation of grades

The grade of the module examination is also the module grade.

Contents

From [EIT-EIS-561-K-7] **Embedded Systems Project** (/mhb/courses/EIT-EIS-561-K-7/):

individuell festgelegte Entwurfs- und Verifikationsaufgaben für Eingebettete Systeme: Applikationssoftware, Systemsoftware, Hardware, Toolentwicklung

Competencies / intended learning achievements

- Eigenständige Bearbeitung komplexer Entwicklungs- und Forschungsaufgaben
- Handhabungswissen für aktuelle Entwurfswerkzeuge: Synthesetools, Verifikationstools, Simulation, Entwicklungsumgebungen für Hardware und Software
- Arbeiten im Team

Requirements for attendance (informal)

Modules:

- [EIT-EIS-571-M-4] Architecture of Digital Systems I (M, 4.0 LP) (/mhb/modules/EIT-EIS-571-M-4/)
- [EIT-EIS-573-M-4] Architecture of Digital Systems II (M, 4.0 LP) (/mhb/modules/EIT-EIS-573-M-4/)

Requirements for attendance (formal)

None

References to Module / Module Number [EIT-EIS-561-M-7]

Course of Study	Section	Choice/Obligation
[EIT-88.781-SG#2010] M.Sc. Electrical and Computer Engineering [2010] (/mhb/FB-EIT/cos-556/)	Elective Subjects	[W] Elective Module
[EIT-88.A44-SG#2018] M.Sc. Media and Communication Technology [2018] (/mhb/FB-EIT/cos-568/)	Technical Elective Subjects	[W] Elective Module
[EIT-88.?-SG#2021] M.Sc. Electrical and Computer Engineering [2021] (/mhb/FB-EIT/cos-686/)	Technical Elective Modules	[W] Elective Module
[EIT-88.?-SG#2021] M.Sc. Media and Communication Technology [2021] (/mhb/FB-EIT/cos-688/)	Technical Elective Modules	[W] Elective Module