

## Module Handbook

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# Module EIT-DEK-008-M-7

Master's Thesis ESY (M, 30.0 LP)

## Module Identification

Module Number	Module Name	CP (Effort)
EIT-DEK-008-M-7	<i>Master's Thesis ESY</i>	30.0 CP (900 h)

## Basedata

CP, Effort	30.0 CP = 900 h
Position of the semester	1 Sem. in WiSe/SuSe
Level	[7] Master (Advanced)
Language	[DE/EN] German or English as required
Module Manager	Potchinkov, Alexander, Prof. Dr.-Ing. (PROF   DEPT: EIT)
Lecturers	The Lecturers of the department Electrical and Computer Engineering
Reference course of study	[EIT-88.D55-SG#2021] M.Sc. Embedded Computing Systems (ESY) [2021]
Lifecycle-State	[NORM] Active

## Study achievement SL1

- Verification of study performance: **colloquium**

## Examination achievement PL1

- Form of examination: **Master's thesis**
- Examination Frequency: each semester

## Evaluation of grades

The grade of the module examination is also the module grade.

## Contents

individual task

## Competencies / intended learning achievements

The Master Thesis shows that the student can solve a given task from the area of Embedded Computing Systems under supervision with scientific methods within a limited amount of time. In addition, he / she can present the results in written and oral form.

## Requirements for attendance of the module (informal)

depends on the given task

## Requirements for attendance of the module (formal)

at least 60 CPs

## References to Module / Module Number [EIT-DEK-008-M-7]

Course of Study	Section	Choice/Obligation
[EIT-88.D55-SG#2021] M.Sc. Embedded Computing Systems (ESY) [2021]	[Thesis] Master's Thesis	[P] Compulsory