

## Module Handbook

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# Course INF-62-81-K-7

Hardware-Software Synthesis (Project) (4L, 8.0 LP)

## Course Type

SWS	Type	Course Form	CP (Effort)	Presence-Time / Self-Study
-	K		8.0 CP	184 h
4	L	Programming training course		56 h
(4L)			8.0 CP	56 h 184 h

## Basedata

SWS	4L
CP, Effort	8.0 CP = 240 h
Position of the semester	1 Sem. in WiSe/SuSe
Level	[7] Master (Advanced)
Language	[EN] English
Lecturers	Schneider, Klaus, Prof. Dr. (PROF   DEPT: INF)
Area of study	[INF-ES] Embedded Systems and Robotics
Lifecycle-State	[NORM] Active

## Possible Study achievement

- Verification of study performance: **presentation**
- Examination number (Study achievement): 66281 ("Project Applied Verification")
- Details of the examination (type, duration, criteria) will be announced at the beginning of the course.

## Contents

Specific Topics are

- synchronous programs
- software synthesis
- hardware synthesis
- runtime environments
- verification and validation

## Literature

Will be provided in the course.

## Requirements for attendance (informal)

### Courses

- [INF-62-36-K-6] Model-based Design of Embedded Systems (4V+2U, 8.0 LP)

## Requirements for attendance (formal)

None

## References to Course [INF-62-81-K-7]

Module	Name	Context	
[INF-62-81-M-7]	Hardware-Software Synthesis (Project)	P: Obligatory	4L, 8.0 LP
Course-Pool	Name		
[INF-ES_P-KPOOL-7]	Projects of the teaching area Embedded Systems and Robotics		